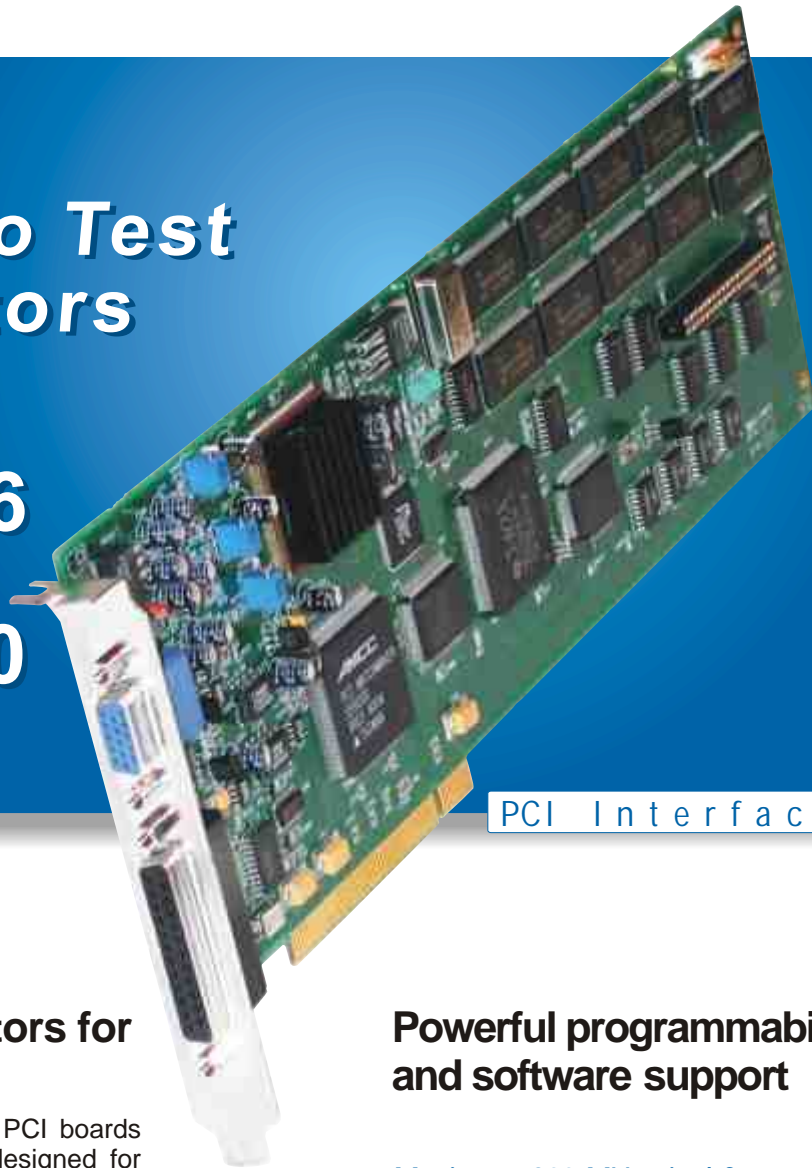


## Video Test Generators

**VTG-3016**

**VTG-3030**



PCI Interface

### Test Signal Generators for Analog Displays

The VTG-3030 and VTG-3016 PCI boards are analog video generators, designed for testing, evaluating and servicing many types of CRT, analog input displays and other devices. They supply all the necessary signals for displaying test patterns at various timing formats required by CRT and other devices with analog inputs.

### Superior tools and software for your testing needs

UNIGRAF VTG software and hardware supply the user with efficient solutions. It is easy to build test sequences for manufacturing, burn-in, quality control and service routines. If your needs are simple, the WinVTG.exe user interface will do everything you need. When you are developing your own ATE applications we will supply the DLLs and Drivers you need.

### Powerful programmability and software support

- Maximum 300 MHz pixel frequency
- WinVTG.exe User Interface for Windows™(95, 98, NT, 2000)
- DLL for application programming
- Bitmap support for multiple file formats: .BMP .GIF .JPEG .PCD .PCX .PNG .TIF
- ATE support, VESA DPMS and DDC
- Unlimited number of permanent programmable patterns, timings, colors, palettes, signal formats and sequences

# UNIGRAF

## Video Test Generators VTG-3030 and VTG-3016

### TECHNICAL SPECIFICATIONS

#### Pixel Clock

■ VTG-3030	4 MHz - 300 MHz
■ VTG-3016	4 MHz - 160 MHz
	Step: 0,01 MHz
	Accuracy: $\pm 50$ ppm

#### Graphics Display Memory Size

■ Resolutions	2048 x 2048 x 8 bit colors out of 16.7 million true color
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#### Horizontal Timing

■ Scan Range	1 kHz - 1000 kHz
■ Period	256 - 4096 pixels
- Adjust step	2 pixels up to 60 MHz
	4 pixels up to 120 MHz
	8 pixels up to 240 MHz
	16 pixels up to 300 MHz
■ Sync Pulse	16 - 2048 pixels
■ Back Porch	16 - 2048 pixels
■ Display Resolution	6 - 4080 pixels, active
■ Adjust Step (not period)	1 pixel for all dot clocks

#### Vertical Timing

■ Scan Range	10 - 200 Hz
■ Period	4 - 4500 lines per field
■ Sync Pulse	1 - 4096 lines
■ Back Porch	0 - 4096 lines
■ Display Resolution	1 - 4096 lines, active
■ Adjust Step	1 line for all parameters
■ Scan Modes	Interlaced / non-interlaced

#### Analog Outputs

■ Analog Video	RGB, 75 termination
■ Video Level	0.0 - 1.000 Vp-p
■ Colors	256 simultaneous colors out of 16.7 million, 24 bit palette.
■ Sync On Green	Selectable on / off. Level 0.0 - 0.47 Vp-p
■ Connectors	15 pin HDD (VGA) and 25 pin D-connector
■ Output Protection	Output buffers with 75 termination and protection diodes.

#### Digital Outputs

■ Separate Syncs	HSync, VSync and CSync with programmable polarity and gating
■ Composite modes	H+V, H exor V, serrated

#### Data Communication

■ DDC2B capable DDC interface with write function
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#### Data Storage

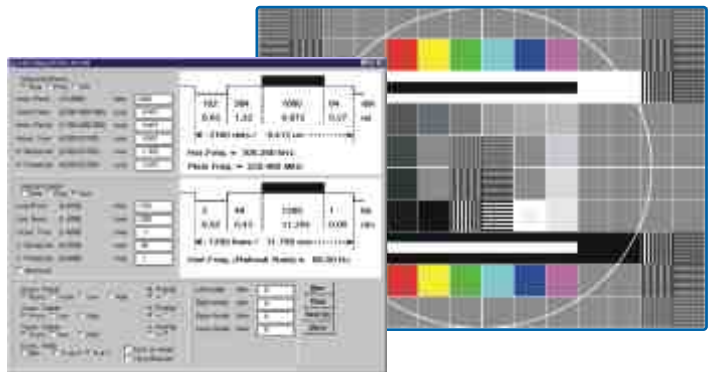
■ Number of Files	only restricted by usable disk capacity
■ Timing Files	unlimited
■ Test Patterns	unlimited
■ Color Set Files	unlimited
■ Test Sequences	unlimited
■ Instruction Files	unlimited

#### Data File Management

■ Default Settings	Programmable timing, pattern, color and sequence files at start. Selection of normal or auto sequence.
■ File Path Setting	Separately programmable for all file types.
■ LAN Control	Possible with standard LAN-software.

#### System Requirements and Software

■ Windows™ operating system (95, 98, NT, 2000)
■ WinVTG .exe User Interface
■ Windows DLL software library
■ Visual Basic and C++ sample programs
■ PCI-bus
■ Power: +5V/3A max, +12V/10mA, -12V/80mA (+ output connector supply for +5V max1A and +12V max1A)
■ EMI: meets EN 55011, Class B
■ Dimensions: 317 mm x 107 mm



### Custom Pattern Programming

The Unigraf VTG Series allows you to create your own patterns with a few simple lines of code. For example:

```
COLOR 15 ; Sets the color of the pattern
REPEAT A 0 10000 500 ; Sets up a loop to be repeated 20 times
LINE A 0A 10000 ; Draws 20 lines from top to bottom of display - left(0) to right (10000)
END
```

You can program patterns in SCALED mode with 10000 x 10000 virtual resolution. The ABSOLUTE mode programming uses actual pixel values from horizontal 0-MaxX and vertical 0-MaxY, respectively. Both pattern types can be used with different timings.



ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE.

